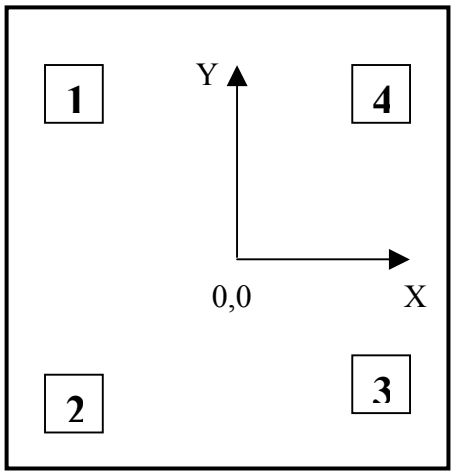
L7815	CHIP FOR THREE-TERMINAL POSITIV VOLTAGE REGULATOR IC

Features:

- Output Current in Excess of 1.5A (Tj=25°C,P_D≤15W)
- No External Components Required
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- Output Transistor Safe-Area Compensation
- Output Voltage Offered in 4% Tolerance

Physical Characteristics:

Wafer Diameter.....100±0,5mm
Wafer Thickness.....280±20 μm
Die size.....1.4 x 1.9 mm^2
Scribe Width.....100 μm
Metallization Bottom.....Ti-Ni-Ag
Ti-Ni – 0.5-0.7 μm
Ag - 0.5-0.7 μm
Passivation.....PSG



- Maximum Input Voltage – 36V
- Operation Junction Temperature Range – -40°C~125°C

Pad #	Pad name	Discription	Bond Pad (μm)	X	Y
1	IN	Input	230x230	-610	247
2	GND	Ground	230x230	-610	-626
3	OUT	Output	230x230	372	-560
4	OUT	Output	230x230	372	247

ELECTRICAL CHARACTERISTICS CHIPS ON WAFER

(Vin=23V, Io=0.5A, Ci=0.33μF, Co=0.1μF, Tj = 25°C, unless otherwise noted.)

Characteristic	Symbol	Test Condition	Min	Max	Unit
Output Voltage	Vo		14.4	15.6	V
Output Voltage	Vo	5.0mA≤Io≤1.0A; 17.5V≤Vin≤30V 0°C ≤Tj≤125°C	14.25	15.75	V
Line Regulation	ΔVv	17.5V ≤ Vin ≤ 30V; 20V ≤ Vin ≤ 26V		300 150	mV
Load Regulation	ΔVi	5.0mA≤Io≤1.5A 0.25A≤Io≤0.75A		300 150	mV
Quiescent Current	Ib			8.0	mA
Quiescent Current Change	ΔIb	17.5V ≤ Vin ≤ 30V, 5mA ≤ Io ≤ 1.0A		1.0 0.5	mA