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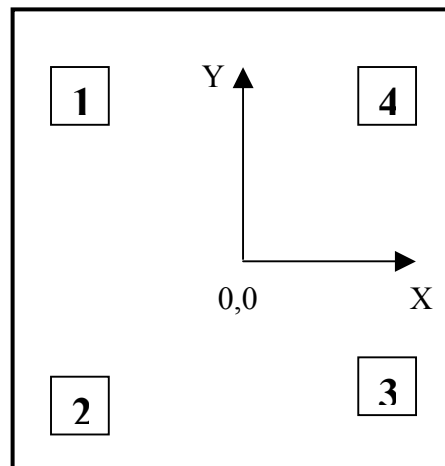
CHIP FOR THREE-TERMINAL POSITIV VOLTAGE REGULATOR IC

Features:

- ◆ Output Current in Excess of 1.5A ($T_j=25^{\circ}\text{C}, P_D \leq 15\text{W}$)
- ◆ No External Components Required
- ◆ Internal Short Circuit Current Limiting
- ◆ Internal Thermal Overload Protection
- ◆ Output Transistor Safe-Area Compensation
- ◆ Output Voltage Offered in 4% Tolerance

Physical Characteristics:

Wafer Diameter.....	100±0,5mm
Wafer Thickness.....	280±20 μm
Die size.....	1.4 x 1.9 mm ²
Scribe Width.....	100 μm
Metallization Bottom.....	Ti-Ni-Ag
	Ti-Ni – 0.5-0.7 μm
	Ag - 0.5-0.7 μm
Passivation.....	PSG



- ◆ Maximum Input Voltage – 36V
- ◆ Operation Junction Temperature Range – $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$

Pad #	Pad name	Discription	Bond Pad (μm)	X	Y
1	IN	Input	230x230	-610	247
2	GND	Ground	230x230	-610	-626
3	OUT	Output	230x230	372	-560
4	OUT	Output	230x230	372	247

ELECTRICAL CHARACTERISTICS CHIPS ON WAFER

(Vin=14V, Io=0.5A, Ci=0.33μF, Co=0.1μF, Tj = 25°C, unless otherwise noted.)

Characteristic	Symbol	Test Condition	Min	Max	Unit
Output Voltage	V _o		7.7	8.3	V
Output Voltage	V _o	5.0mA ≤ I _o ≤ 1.0A; 10.5V ≤ V _{in} ≤ 23V 0°C ≤ T _j ≤ 125°C	7.6	8.4	V
Line Regulation	ΔV _v	10.5V ≤ V _{in} ≤ 25V; 11V ≤ V _{in} ≤ 17V		160 80	mV
Load Regulation	ΔV _i	5.0mA ≤ I _o ≤ 1.5A 0.25A ≤ I _o ≤ 0.75A		160 80	mV
Quiescent Current	I _b			8.0	mA
Quiescent Current Change	ΔI _b	10.5V ≤ V _{in} ≤ 25V, 5mA ≤ I _o ≤ 1.0A		1.0 0.5	mA